

TIME TABLE

Semester: ODD Year: 2026-2027

Class: B. TECH-II VL-A

Day	1	2	3	4		5	6	7	8(EXTRA)
	8.30 - 9.20	9.30 - 10.20	10.30 - 11.20	11.30 - 12.20	12.20 - 2.00	2.00 - 2.50	3.00 - 3.50	4.00 - 4.50	5.00 - 5.50
Monday	A1 (DSP)	B2 (AC)	C3 (M&M)	D4 (PEEBM)					
						P11 (DSP-A1 & M&M-A2)		P12 (AC-A1)	
Tuesday	B1 (DSP)	C2 (M&M)	D3 (CS)	E4 (PEEBM)					
Wednesday	C1 (PEEBM)	D2 (CS)							
			P6 (AC-A2 & M&M-A1)					P16 (DSP-A2)	
Thursday	D1 (AC)	E2 (CS)	A3 (M&M)						
Friday	E1 (PEEBM)	A2 (AC)	B3 (DSP)	C4 (CS)					

Sr. No.	SLOT	COURSE NO	TITLE	FACULTY	L-T-P	CLASS ROOM
(1)	B2,D1,A2	VL203	Analog Circuits	PD,PD,PD	3-0-2	EC021N
(2)	A1,B1,B3	VL201	Digital Signal Processing	SD,SD,SD	3-1-0	EC021N
(3)	C3,C2,A3	VL205	Microprocessors and Microcontrollers		3-0-2	EC021N
(4)	D3,D2,E2, (T)C4	EE259	Control Systems	ST,ST,ST	3-0-2	EC021N
(5)	D4,E4,C1, E1	MG210	Professional Ethics, Economics, and Business Management		3-1-0	EC021N
(6)	P12	VL203	Analog Circuits-A1	DJ,PD, L1	3-0-2	EC302N
(7)	P6	VL203	Analog Circuits-A2	PS,L1	3-0-2	EC302N
(8)	P6	VL205	Microprocessors and Microcontrollers-A1	PKS,L4,L6	3-0-2	EC307N
(9)	P11	VL205	Microprocessors and Microcontrollers-A2	PKS,L4,L7	3-0-2	EC307N
(10)	P11	VL201	Digital Signal Processing-A1	SD,L8,P9	3-0-2	EC406N
(11)	P16	VL201	Digital Signal Processing-A2	SD,L8,P9	3-0-2	EC406N

